

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L14	77	l12 and (security same password\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 16:37
L13	145	l12 and access\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 16:37
S4	29	S3 and @ad<"20011101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 16:36
L12	154	l11 and @ad<"20011101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 16:36
L11	338	(security same bridge) and PCI	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 16:36
L10	26	l9 and bridge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 16:35
L9	172	726/34.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 16:32
L8	4	l7 and bridge	US-PGPUB; USPAT; USOCR	OR	OFF	2006/08/01 15:31
L7	44	("4405829"   "4799258"   "4890323"   "4969188"   "5276737"   "5315658"   "5375243"   "5377269"   "5432851"   "5485519"   "5511122"   "5537540"   "5590199"   "5666415").PN. OR ("5949882").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/08/01 15:31

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L6	2	"5953422".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 14:59
L5	0	"5953422.pn."	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 14:59
L4	26	"5953422"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 14:59
L3	4	l2 and bridge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 13:57
L2	18	(US-20020080959-\$ or US-20020174287-\$ or US-20030097587-\$).did. or (US-6484262-\$ or US-6332193-\$ or US-6032257-\$ or US-6799155-\$ or US-6925560-\$ or US-6669096-\$ or US-6351817-\$ or US-6243813-\$ or US-6189100-\$ or US-6901375-\$ or US-6389542-\$ or US-6304970-\$ or US-5949882-\$ or US-4799258-\$).did. or (US-20020138677-\$).did.	US-PGPUB; USPAT; DERWENT	OR	OFF	2006/08/01 13:52
S32	2	"6662251".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 12:01
S31	2	"20030097587"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 10:58
S1	1096	713/168.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/01 10:58

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S30	2	"6351817".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 10:06
S29	39	("4405829"   "4799258"   "4890323"   "4969188"   "5276737"   "5315658"   "5375243"   "5377269"   "5432851"   "5485519"   "5511122"   "5537540"   "5590199"   "5666415").PN. OR ("5949882").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/18 09:19
S28	2	"5949882".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/18 09:19
S27	31	S26 and @ad<"20011101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/17 15:03
S26	45	((SMM system adj management adj mode) SAME (power)) and (authenticat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/17 15:03
S25	4	(SMM system adj management adj mode) SAME (power) SAME (authenticat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/17 15:03
S6	4	S5 and @ad<"20011101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/17 15:03
S24	2	"6389542".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/17 14:53
S23	0	"6389542.pn."	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/17 12:42

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S22	11	"6389542"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/17 12:42
S21	126	S20 and @ad<"20011101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/13 16:58
S20	237	(micro\$1controller with authentic\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/13 16:56
S19	2	"5327497".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/13 14:14
S18	4	S17 and @ad<"20011101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/13 14:14
S17	26	S16 and bio\$1metric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/13 14:13
S16	237	(micro\$1controller with authentic\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/13 13:29
S2	3	S1 and (micro\$1controller with authentic\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/13 13:28
S15	0	S13 and controller	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/13 13:02
S14	0	S13 and micro\$1controller	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/13 13:02

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S13	8	("5036461"   "5327497"   "5390331"   "5521362"   "5844497"   "5901303"   "5987134"   "6351817").PN. OR ("6669096").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/13 13:02
S12	18	("4696449"   "5055827"   "5154456"   "5287519"   "5349643"   "5475839"   "5574786"   "5632165"   "5680547"   "5691928"   "5796942"   "5826015"   "5892906"   "5978912"   "6189100"   "6243813"   "6263388").PN. OR ("6484262").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/12 18:24
S11	18	("4641349"   "5131038"   "5280527"   "5291560"   "5613004"   "5706349"   "5768426"   "5809139"   "5822432"   "5825892").PN. OR ("6332193").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/12 18:23
S10	21	S9 and @ad<"20011101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/12 17:47
S8	81	S7 and @ad<"20011101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/12 17:47
S9	48	((embedded adj (\$6processor \$6controller)) same authentication)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/12 17:46
S7	189	(embedded with (\$6processor \$6controller)) same (authentication)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/12 17:46
S5	6	(embedded with (\$6processor \$6controller)) same (authentication with request)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/12 17:39

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S3	54	((micro\$1controller micro\$1processor) same (authentic\$5 with request))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/12 17:22
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# Inventor Information for 10/084596

Inventor Name	City	State/Country
GULICK, DALE E.	AUSTIN	TEXAS

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**Inventor Name Search Result**

Your Search was:

Last Name = GULICK

First Name = DALE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07564048</u>	Not Issued	166	08/08/1990	PROGRAMMABLE KEYPAD MONITOR	GULICK, DALE
<u>07569935</u>	Not Issued	166	08/20/1990	POWER-EFFICIENT EXTERNAL MEMORY ACCESS CONTROL USING EXTERNAL MEMORY ENABLE TIME DURATIONS INDEPENDENT OF EXTERNAL MEMORY ACCESSING RATE	GULICK, DALE
<u>07942359</u>	<u>5266950</u>	150	09/09/1992	PROGRAMMABLE KEYPAD MONITOR	GULICK, DALE
<u>08140820</u>	<u>5481690</u>	150	10/04/1993	POWER-EFFICIENT EXTERNAL MEMORY ACCESS CONTROL USING EXTERNAL MEMORY ENABLE TIME DURATIONS INDEPENDENT OF EXTERNAL MEMORY ACCESSING RATE	GULICK, DALE
<u>08264883</u>	<u>5535281</u>	150	06/24/1994	ANALOG INTEGRATED CIRCUIT	GULICK, DALE
<u>08479780</u>	Not Issued	168	06/07/1995	COMPUTER SYSTEM AND METHOD FOR TRANSFERRING COMMANDS AND DATA TO A DEDICATED MULTIMEDIA ENGINE	GULICK, DALE
<u>08479781</u>	Not Issued	161	06/07/1995	PROGRAMMABLE BUS ARBITER INCLUDING REAL TIME PRIORITY INDICATOR FIELDS FOR ARBITRATION PRIORITY SELECTION	GULICK, DALE
<u>08481705</u>	Not Issued	166	06/07/1995	COMPUTER SYSTEM HAVING A DEDICATED MULTIMEDIA ENGINE INCLUDING MULTIMEDIA MEMORY	GULICK, DALE
<u>08481706</u>	Not Issued	161	06/07/1995	COMPUTER SYSTEM HAVING AN IMPROVED BUS ARBITER ADAPTED FOR REAL TIME APPLICATIONS	GULICK, DALE
<u>08878229</u>	<u>5732224</u>	250	06/18/1997	COMPUTER SYSTEM HAVING A DEDICATED MULTIMEDIA ENGINE INCLUDING MULTIMEDIA MEMORY	GULICK, DALE



<u>08948639</u>	<u>5870622</u>	150	10/10/1997	COMPUTER SYSTEM AND METHOD FOR TRANSFERRING COMMANDS AND DATA TO A DEDICATED MULTIMEDIA ENGINE	GULICK, DALE
<u>09220487</u>	<u>6167492</u>	150	12/23/1998	CIRCUIT AND METHOD FOR MAINTAINING ORDER OF MEMORY ACCESS REQUESTS INITIATED BY DEVICES COUPLED TO A MULTIPROCESSOR SYSTEM	GULICK, DALE
<u>09382360</u>	<u>6389526</u>	150	08/24/1999	CIRCUIT AND METHOD FOR SELECTIVELY STALLING INTERRUPT REQUESTS INITIATED BY DEVICES COUPLED TO A MULTIPROCESSOR SYSTEM	GULICK, DALE
<u>09832374</u>	Not Issued	161	04/11/2001	Soft magnetic compound for injection molded return paths	GULICK, DALE
<u>10718166</u>	Not Issued	30	11/20/2003	Soft magnetic compound for injection molded return paths	GULICK, DALE
<u>09098570</u>	<u>6134698</u>	150	06/17/1998	REDUCED PIN COUNT ISOCRONOUS DATA BUS	GULICK, DALE E.
<u>08929153</u>	<u>6601178</u>	150	09/11/1997	SYSTEM POWER MANAGEMENT PARTITIONED ACROSS A SERIAL BUS	GULICK, DALE E
<u>09094016</u>	<u>6502123</u>	150	06/09/1998	ISOCRONOUS SYSTEM USING CERTIFIED DRIVERS TO ENSURE SYSTEM STABILITY	GULICK, DALE E
<u>09501888</u>	<u>6195749</u>	150	02/10/2000	Computer system including a memory access controller for using non-system memory storage resources during system boot time	GULICK, DALE E.
<u>09502068</u>	<u>6532019</u>	150	02/10/2000	INPUT/OUTPUT INTEGRATED CIRCUIT HUB INCORPORATING A RAMDAC	GULICK, DALE E.
<u>09544858</u>	<u>6952751</u>	150	04/07/2000	METHOD AND APPARATUS FOR EXTENDING LEGACY COMPUTER SYSTEMS	GULICK, DALE E.
<u>09564765</u>	Not Issued	93	05/04/2000	MEMORY CONTROLLER PROVIDING MULTIPLE POWER MODES FOR ACCESSING MEMORY DEVICES BY REORDERING MEMORY TRANSACTIONS	GULICK, DALE E.
<u>09577523</u>	Not Issued	161	05/24/2000	Graphics subsystem including a RAMDAC IC with a dedicated vsync interrupt structure for connection to a graphics bus	GULICK, DALE E.
<u>09577524</u>	Not Issued	161	05/24/2000	Graphics subsystem including a pull-mode RAMDAC for connection to a graphics bus	GULICK, DALE E.

<u>09577525</u>	Not Issued	161	05/24/2000	Graphics subsystem including a dual pull-mode RAMDAC for connection to a graphics bus	GULICK, DALE E.
<u>09577526</u>	Not Issued	161	05/24/2000	Graphics subsystem including a RAMDAC IC including integrated video overlay circuitry for connection to a graphics bus	GULICK, DALE E.
<u>09577527</u>	6798418	150	05/24/2000	GRAPHICS SUBSYSTEM INCLUDING A RAMDAC IC WITH DIGITAL VIDEO STORAGE INTERFACE FOR CONNECTION TO A GRAPHICS BUS	GULICK, DALE E.
<u>09636417</u>	Not Issued	161	08/10/2000	High speed data bus incorporating series resistor coupled bus devices	GULICK, DALE E.
<u>09652647</u>	6760852	150	08/31/2000	SYSTEM AND METHOD FOR MONITORING AND CONTROLLING A POWER-MANAGEABLE RESOURCE BASED UPON ACTIVITIES OF A PLURALITY OF DEVICES	GULICK, DALE E.
<u>09702147</u>	6385705	150	10/30/2000	CIRCUIT AND METHOD FOR MAINTAINING ORDER OF MEMORY ACCESS REQUESTS INITIATED BY DEVICES IN A MULTIPROCESSOR SYSTEM	GULICK, DALE E.
<u>09715946</u>	Not Issued	161	11/17/2000	System and method for enabling enumeration of physically partitioned devices	GULICK, DALE E.
<u>09747016</u>	Not Issued	167	12/20/2000	MONOLITHIC PC AUDIO CIRCUIT	GULICK, DALE E.
<u>09772423</u>	6473663	150	01/29/2001	NOISE ELIMINATION IN A USB CODEC	GULICK, DALE E.
<u>09798456</u>	6816935	150	03/02/2001	INTERRUPT AND STATUS REPORTING STRUCTURE AND METHOD FOR A TIMESLOT BUS	GULICK, DALE E.
<u>09798457</u>	Not Issued	161	03/02/2001	Chipset architecture with plug-and-play configuration registers	GULICK, DALE E.
<u>09798458</u>	Not Issued	161	03/02/2001	Time division bus host controller architecture	GULICK, DALE E.
<u>09798480</u>	Not Issued	161	03/02/2001	Data protocol structure for a time division bus	GULICK, DALE E.
<u>09852372</u>	7065654	150	05/10/2001	SECURE EXECUTION BOX	GULICK, DALE E.
<u>09852942</u>	Not Issued	121	05/10/2001	Computer system architecture for enhanced security and manageability	GULICK, DALE E.
<u>09853225</u>	Not Issued	71	05/11/2001	Secure system mode duration management	GULICK, DALE E.
<u>09853234</u>	6862641	150	05/11/2001	INTERRUPTABLE AND RE-ENTERABLE SYSTEM MANAGEMENT MODE PROGRAMMING CODE	GULICK, DALE E.

<a href="#">09853335</a>	Not Issued	124	05/11/2001	Asset sharing between host processor and security hardware	GULICK, DALE E.
<a href="#">09853395</a>	Not Issued	41	05/11/2001	Enhanced security and manageability using secure storage in a personal computer system	GULICK, DALE E.
<a href="#">09853437</a>	<a href="#">6832317</a>	150	05/11/2001	PERSONAL COMPUTER SECURITY MECHANISM	GULICK, DALE E.
<a href="#">09853443</a>	Not Issued	149	05/11/2001	Protection mechanism for biometric input data	GULICK, DALE E.
<a href="#">09853446</a>	<a href="#">7043581</a>	150	05/11/2001	RESOURCE SEQUESTER MECHANISM	GULICK, DALE E.
<a href="#">09853447</a>	<a href="#">6823451</a>	150	05/11/2001	INTEGRATED CIRCUIT FOR SECURITY AND MANAGEABILITY	GULICK, DALE E.
<a href="#">09854040</a>	<a href="#">6968460</a>	150	05/11/2001	CRYPTOGRAPHIC RANDOMNESS REGISTER FOR COMPUTER SYSTEM SECURITY	GULICK, DALE E.
<a href="#">09870889</a>	Not Issued	95	05/30/2001	EXTERNAL LOCKING MECHANISM FOR PERSONAL COMPUTER MEMORY LOCATIONS	GULICK, DALE E.
<a href="#">09870890</a>	<a href="#">7007300</a>	150	05/30/2001	SECURE BOOTING OF A PERSONAL COMPUTER SYSTEM	GULICK, DALE E.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09871084</u>	7003676	150	05/30/2001	LOCKING MECHANISM OVERRIDE AND DISABLE FOR PERSONAL COMPUTER ROM ACCESS PROTECTION	GULICK, DALE E.
<u>09871511</u>	Not Issued	61	05/30/2001	Monotonic counter mechanism for computer system security	GULICK, DALE E.
<u>09904373</u>	Not Issued	95	07/11/2001	METHOD AND APPARATUS FOR CONFIGURING A PERIPHERAL BUS	GULICK, DALE E.
<u>09904374</u>	6671748	150	07/11/2001	METHOD AND APPARATUS FOR PASSING DEVICE CONFIGURATION INFORMATION TO A SHARED CONTROLLER	GULICK, DALE E.
<u>09933290</u>	6625743	150	08/20/2001	A METHOD FOR SYNCHRONIZING GENERATION AND CONSUMPTION OF ISOCRONOUS DATA	GULICK, DALE E.
<u>09998758</u>	6836813	150	11/30/2001	SWITCHING I/O NODE FOR CONNECTION IN A MULTIPROCESSOR COMPUTER SYSTEM	GULICK, DALE E.
<u>10005648</u>	Not Issued	71	12/03/2001	Embedded processor supporting both ACPI and ASF operations	GULICK, DALE E.
<u>10023233</u>	7003607	150	03/20/2002	MANAGING A CONTROLLER EMBEDDED IN A BRIDGE	GULICK, DALE E.
<u>10033142</u>	6963948	150	11/01/2001	MICROCOMPUTER BRIDGE ARCHITECTURE WITH AN EMBEDDED MICROCONTROLLER	GULICK, DALE E.
<u>10034560</u>	6791554	150	12/27/2001	I/O NODE FOR A COMPUTER SYSTEM INCLUDING AN INTEGRATED GRAPHICS ENGINE	GULICK, DALE E.
<u>10034878</u>	6697890	150	12/27/2001	I/O NODE FOR A COMPUTER SYSTEM INCLUDING AN INTEGRATED I/O INTERFACE	GULICK, DALE E.
<u>10034967</u>	6857033	150	12/27/2001	I/O NODE FOR A COMPUTER SYSTEM INCLUDING AN INTEGRATED GRAPHICS ENGINE AND AN INTEGRATED I/O HUB	GULICK, DALE E.

<u>10044707</u>	Not Issued	61	01/11/2002	Processing tasks with failure recovery	GULICK, DALE E.
<u>10045117</u>	Not Issued	95	11/01/2001	MICROCOMPUTER BRIDGE FOR REMOTE MANAGEABILITY	GULICK, DALE E.
<u>10062390</u>	Not Issued	161	02/01/2002	Serial pin reflection of GPIO terminals	GULICK, DALE E.
<u>10066879</u>	Not Issued	93	02/04/2002	ASF STATE DETERMINATION USING CHIPSET-RESIDENT WATCHDOG TIMER	GULICK, DALE E.
<u>10066948</u>	Not Issued	120	02/04/2002	Remote management mechanism to prevent illegal system commands	GULICK, DALE E.
<u>10067175</u>	6892332	150	02/04/2002	HARDWARE INTERLOCK MECHANISM USING A WATCHDOG TIMER	GULICK, DALE E.
<u>10084596</u>	Not Issued	71	02/27/2002	Embedded processor with direct connection of security devices for enhanced security	GULICK, DALE E.
<u>10133297</u>	Not Issued	161	04/26/2002	Semiconductor device and method of controlling clock speed of a semiconductor device	GULICK, DALE E.
<u>10159346</u>	Not Issued	61	05/31/2002	Process load balancing	GULICK, DALE E.
<u>10180670</u>	6889262	150	06/26/2002	DIRECT TRANSACTION MODE FOR PERIPHERAL DEVICES	GULICK, DALE E.
<u>10184407</u>	6823403	150	06/27/2002	DMA MECHANISM FOR HIGH-SPEED PACKET BUS	GULICK, DALE E.
<u>10185171</u>	Not Issued	41	06/28/2002	Message based power management in a multi-processor system	GULICK, DALE E.
<u>10185195</u>	7051218	150	06/28/2002	MESSAGE BASED POWER MANAGEMENT	GULICK, DALE E.
<u>10198637</u>	Not Issued	71	07/18/2002	Use of a signal line to adjust width and/or frequency of a communication link during system operation	GULICK, DALE E.
<u>10209484</u>	6998870	150	07/31/2002	METHOD AND APPARATUS FOR IMPEDANCE MATCHING IN SYSTEMS CONFIGURED FOR MULTIPLE PROCESSORS	GULICK, DALE E.
<u>10283554</u>	Not Issued	41	10/30/2002	USB host controller	GULICK, DALE E.
<u>10283555</u>	6990550	150	10/30/2002	TRANSACTION DURATION MANAGEMENT IN A USB HOST CONTROLLER	GULICK, DALE E.
<u>10283612</u>	Not Issued	93	10/30/2002	SERIAL BUS HOST CONTROLLER DIAGNOSIS	GULICK, DALE E.
<u>10283733</u>	6944725	150	10/30/2002	RECIPROCALLY ADJUSTABLE DUAL QUEUE MECHANISM	GULICK, DALE E.
<u>10419082</u>	Not Issued	30	04/18/2003	Computer system including a secure execution mode-capable CPU and a security services processor connected via a secure communication path	GULICK, DALE E.

<a href="#">10419121</a>	Not Issued	30	04/18/2003	Initialization of a computer system including a secure execution mode-capable processor	GULICK, DALE E.
<a href="#">10429132</a>	Not Issued	30	05/02/2003	Computer system including a bus bridge for connection to a security services processor	GULICK, DALE E.
<a href="#">10464966</a>	Not Issued	71	06/19/2003	Controlling the replacement of prefetched descriptors in a cache	GULICK, DALE E.
<a href="#">10628715</a>	Not Issued	20	07/28/2003	Flexible probe/probe response routing for maintaining coherency	GULICK, DALE E.
<a href="#">10647397</a>	Not Issued	41	08/25/2003	Configuring a communication link interface	GULICK, DALE E.
<a href="#">10654734</a>	Not Issued	41	09/04/2003	Computer system employing a trusted execution environment including a memory controller configured to clear memory	GULICK, DALE E.
<a href="#">10842296</a>	Not Issued	83	05/10/2004	System including a host connected to a plurality of memory modules via a serial memory interconnect	GULICK, DALE E.
<a href="#">60306361</a>	Not Issued	159	07/18/2001	Message based power management	GULICK, DALE E.
<a href="#">60373571</a>	Not Issued	159	04/18/2002	Secure execution mode functional specification	GULICK, DALE E.
<a href="#">60470078</a>	Not Issued	159	05/13/2003	Memory interface	GULICK, DALE E.
<a href="#">07035683</a>	Not Issued	166	04/03/1987	DATA LINK CONTROLLER WITH FLEXIBLE MULTIPLEXER	GULICK, DALE E.
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